

# **Rapidio The Embedded System Interconnect**

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Problems with Traditional Buses The Market Problem  
RapidIO: A New Approach... The Interconnect Problem -  
RapidIO®: The Embedded System ... Specifically,  
RapidIO is targeted at the intra-system interconnect  
applications in the high performance embedded  
equipment market. This market has distinctly different  
requirements from the desktop and server computer  
spaces. The embedded market has historically been  
served by a number of different vendors. RapidIO: The  
Interconnect Architecture for High ... - Parallel 8-bit  
RapidIO @ 500 MHz applied clock • Initial application in  
embedded systems - Compute, defense, networking &  
telecom line cards - CPU I/O, Line-card aggregation,  
backplane - Serial PHY allowed expansion to data plane

• Flow control, encapsulation, streams • Positioning – Best solution for embedded systems ~ Embedded Systems - RapidIO The RapidIO architecture is a high-performance packet-switched interconnect technology. RapidIO supports messaging, read/write and cache coherency semantics. RapidIO fabrics guarantee in-order packet delivery, enabling power- and area-efficient protocol implementation in hardware. Based on industry-standard electrical specifications such as those for Ethernet, RapidIO can be used as a chip-to-chip, board-to-board, and chassis-to-chassis interconnect. The protocol is marketed as: RapidIO - the u RapidIO - Wikipedia Overview: Praesum Communications provides a complete solution for high-

speed, low-latency system interconnect requirements. Based on the RapidIO 4.1 standard we have endpoint and switching IP cores that are silicon proven and run at speed on state of the art FPGA platforms from Intel and Xilinx. RapidIO.org Membership Directory The 25xN RapidIO Specification 4.1 offers 100 gigabits per second (Gbps) per x4 port to provide a proven, open standard interconnect fabric for systems requiring heterogeneous scale out and non-volatile storage. It supports interoperability with standard multiprocessing application programming interfaces (APIs), such as RDMA, MPI and MCAPI. Mobiveil's 25xN RapidIO 4.1 compliant controller IP ... RapidIO®, the unified fabric for performance critical computing, addresses needs in

Data Center & HPC, Communications Infrastructure, Industrial Automation and Military & Aerospace markets by offering high reliability, increased bandwidth, and low latency in an intra-system unified fabric. About Us | RapidIO.org RapidIO is a high-performance, packet-switched interconnect technology targeted towards processor-to-processor and board-to-board interconnection in embedded systems. Simulative Analysis of the RapidIO Embedded Interconnect ... RapidIO - The Embedded System Interconnect brings together one essential volume on RapidIO interconnect technology, providing a major reference work for the evaluation and understanding of RapidIO. Covering essential aspects of the



specification, it also answers most usage questions from both hardware and software engineers. RapidIO : the embedded system interconnect (eBook, 2005 ... RapidIO is the only next generation intra-system interconnect with a low overhead messaging protocol. An interconnect protocol for embedded systems must be designed for reliable transactions. Because these systems are typically working on real time data flows, error recovery should be managed in hardware to reduce recovery latency and simplify software design. Rapid IO fits interconnect requirements for embedded systems RapidIO - The Embedded System Interconnect brings together one essential volume on RapidIO interconnect technology, providing a major

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